

Electronic Acknowledgement Receipt

EFS ID:	2717336
Application Number:	10684348
International Application Number:	
Confirmation Number:	4702
Title of Invention:	Integrated mechanism for suspension and deallocation of computational threads of execution in a processor
First Named Inventor/Applicant Name:	Kevin D. Kissell
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Filer Authorized By:	James Weldon Huffman
Attorney Docket Number:	MIPS.0189-00-US
Receipt Date:	15-JAN-2008
Filing Date:	10-OCT-2003
Time Stamp:	12:53:33
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement Letter	MIPS-0189-00-US_LETTER.pdf	23329 525ea877066dd7f1187ac14542705c69fb3631bf1	no	1

Warnings:

Information:

2	Information Disclosure Statement (IDS) Filed	MIPS-0189-00-US_IDS.pdf	31846 d5e9526f58d7a938844f54c6f75382188fbbeb37	no	1
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	NPL Documents	DORAI_Transparent_Thread s_Resource_Sharing_in_SM T_Processors_for_High_Sin gle-Thread_Performance.pdf	123757 b7cf4ac3f1d703c027f70f4ddc1c4fc6f4583b84	no	12
Warnings:					
Information:					
4	NPL Documents	ZILES_The_Use_of_Multithr eading_for_Exception_Handl ing.pdf	70947 a5c82a40279061ab28aa90c8ef604174510c10e3	no	11
Warnings:					
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Total Files Size (in bytes):			249879		
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